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Lightweight low-latency virtual networking

Final talk for the Bachelor's Thesis by

Alexander Daichendt

advised by Florian Wiedner, Jonas Andre

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Chair of Network Architectures and Services Department of Informatics Technical University of Munich



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Research Questions



- RQ1: Are containers viable for URLLC1?
- RQ2: How large is the difference in network latencies between containers and VMs?
- RQ3: What are the root causes for the differences, and what can we do to minimize them?

¹S. Gallenmüller et al. "5G URLLC: A Case Study on Low-Latency Intrusion Prevention". In: IEEE Communications Magazine 58.10 (Okt. 2020), S. 35–41. DOI: 10.1109/HC0H.001.2000467.

Background information

Containers

- shares kernel with host
- no abstraction layers for system calls and paging \rightarrow more performance
- rapid deployment and startup
 - \Rightarrow lightweight virtualization



Background information

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Relevant kernel-level features

Control groups (cgroups): manage hardware resources Examples:

- cpuset.cpus = $8-10 \rightarrow \text{limit cores}$
- memory.max = $34359738368 \rightarrow$ memory limit in bytes
- pids.max = $1000 \rightarrow$ limits creation of new processes

Namespaces: restricts a process' view on the system

Related work

Performance Enhancement of Virtualized Media Gateway with DPDK for 5G Multimedia Communications²

- network latencies with Docker and DPDK based media gateway for the 5G core network
- not directly comparable \rightarrow adjusts Layer 3 and 4 information; no optimizations

Ducked Tails: Trimming the Tail Latency of(f) Packet Processing Systems³

- presents low-latency kernel optimizations \rightarrow fundamental for our measurements
- compares latencies of a DPDK forwarder for bare metal and VM
- not directly comparable \rightarrow lower packet rates

HVNet: Hardware-Assisted Virtual Networking on a Single Physical Host⁴

- virtual networking on a single host with VMs
- framework for our implementation

²W. Chen et al. 'Performance Enhancement of Virtualized Media Gateway with DPDK for 5G Multimedia Communications', In: 2019 International Conference on Intelligent Computing and its Emerging Applications (ICEA). IEEE, Aug. 2019. Doi: 10.1109/icea.2019.855303. URL: https://doi.org/10.1109/icea.2019.855303.

³ S. Gallenmüller et al. "Ducked Tails: Trimming the Tail Latency of(f) Packet Processing Systems". In: 3rd International Workshop on High-Precision, Predictable, and Low-Latency Networking (HIPNet 2021). Izmir, Turkey, Okt. 2021.

⁴ F. Wiedner et al. "HVNet: Hardware-Assisted Virtual Networking on a Single Physical Host". In: IEEE INFOCOM WKSHPS: Computer and Networking Experimental Research using Testbeds (CNERT 2022) (INFOCOM WKSHPS CNERT 2022). Virtual Event, Mai 2022.

Approach



Integration of containers into pos - Setup



Approach



Integration of containers into pos - vBMC



Approach

Integration of containers into HVNet

Idea:

- spawn containers instead of VMs
- execute the VM measurement scripts

Challenges:

- unable to load kernel modules in a container
- unable to create huge pages in a container
- \Rightarrow initialization of kernel-critical resources on container-host

Experiment setup



LoadGen:

- generates UDP traffic with MoonGen
- minimum packet size of 64 B

Timestamper:

- timestamps every single packet with passive optical TAPs
- writes result into pcap file



Impact of interrupt recording (IR) on LXC



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base-nohz 1,52 Mpkt/s



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base-nohz 6,24 Mpkt/s



- · integrated containers in testbed and implemented low-latency tooling
- LXC performs identical compared to VMs and bare metal
- average latencies of Debian Buster and Bullseye differ by 1,4 μs
- software running on the host influences LXC, but not VMs
- a real-time kernel stabilizes latencies

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Conclusion

Comparison & Future work

Comparison:

	LXC	MGW⁵	HiPNet ⁶	HVNet ⁷
VM	×	×	\checkmark	\checkmark
container	\checkmark	\checkmark	×	×
optimizations	\checkmark	×	\checkmark	\checkmark
L3 + 4 adjustment	×	\checkmark	×	×
Debian version	11	0	10	10
latency in µs; 99 th perc.	7.1	> 69 ⁸	3.3	5.5

Future work:

- CGroups v1 with Buster
- Experiments with SR-IOV
- · Flow based experiments
- Comparison with other container solutions

⁵ W. Chen et al., "Performance Enhancement of Virtualized Media Gateway with DPDK for 5G Multimedia Communications".

⁶S. Gallenmüller et al., "Ducked Tails: Trimming the Tail Latency of(f) Packet Processing Systems".

⁷ F. Wiedner et al., "HVNet: Hardware-Assisted Virtual Networking on a Single Physical Host".

⁸ Paper specifies an average latency of 69 µs

Bonus Slides

Discussion preliminary results



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Bonus Slides Low packet rates

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Bonus Slides



5000 worst-case latencies for the base experiment with nohz kernel - 1,52 Mpkt/s



Bonus Slides



5000 worst-case latencies for the base experiment with nohz kernel - 6,24 Mpkt/s

